SPECIFICATION

o Amend paragraph [0017] as follows:

[0017] Sections 200a and 200b may be adapted to operate as follows. Upon sensing that the output of VCO 105 needs to be re-tuned to a new frequency a microcontroller (which may comprise unit 700 or another microcontroller (not shown)), sends instructions to divide-by-N counter 106 to reprogram it. The microcontroller also sends instructions to switch 101 and switch 102 causing them to connect/disconnect components of PLL 100. Upon receiving these instructions, switches 101 and 102 are adapted to open and close, respectively. When so positioned switches 101,102, first tuning section 400 and look-up table 600 are thereafter adapted to re-tune (i.e., adjust the control voltage) of VCO 105. This provides a fast "coarse" adjustment to the control voltage and therefore, to the frequency of operation of VCO 105. This coarse adjustment is substantially within the range of a newly desired operating frequency. This completely bypasses the loop filter (i.e., capacitor 107, resistors 108 and capacitor 109), which would ordinarily be used to generate a DC control voltage, as well as PFD [[103]] 104 and charge pump output. Following this, switches 101 and 102 are adapted to revert to their original state upon receiving instructions from a microcontroller or the like (again, either unit 700 or another microcontroller). The PLL 100 may thereafter be adapted to further adjust the control voltage or enter a frequency/phase lock mode while maintaining the frequency stability of the VCO 105 (i.e., "fine tune", or revert to "normal" operation).